

Department of Electrical and Electronics Engineering

Date: 22.09.2023

Minutes of Board of Studies Meeting

Board of Studies (BoS) meeting of B.Tech., Electrical and Electronics Engineering programme was conducted on 21.09.2023 in virtual mode from 01.30 am to 4.00pm.

All the internal members of VFSTR attended the meeting in person while all the external members participated the meeting virtually with link:

Zoom Link:

https://us07web.zoom.us/j/41386134146?pwd=OWMwa2lNWjFETmYwNW5vam1SbzRZQT19

Agenda of the BoS Meeting:

- 1. Finalization of the NPTEL courses offered as Electives.
- 2. Introduction of new Department elective "Deep Learning for Electrical Engineering".
- 3. Stakeholders feedback is analyzed thoroughly and the curriculum follows the choice based credit system (CBCS).
- 4. Any other points with the permission of Chairperson.

The following members were present

SI.	Name of the Faculty	Designation & Addresses	Position
1	Dr. Polamraju V.S. Sobhan	Assoc. Professor & HoD	Chairperson
2	Dr. M. Umamaheswara Rao	Asst. Professor	Member Secretary
3	Dr. K. Mercy Rosalina	Professor	Member
4	Dr. G. Srinivasa Rao	Professor & Dean R&D	Member
5	Dr. M. Subba Rao	Assoc. Professor	Member
6	Dr. K. Balakrishna	Assoc. Professor	Member
7	Dr. A.R. Vijay Babu	Assoc. Professor	Member
8	Dr. Jithendranath J.	Senior Project Engineer, Hitachi Energy Technology Services Ltd Grid and Power Quality Solutions, Chennai +91-9949537586 jithendranath.j@hitachienergy.com	Member
9	Dr. B. Satish Babu	Sr. Staff Engineer, Infineon Technologies, Bangalore, 9958006750 satishbabu.bhogineni@infineon.com	Special Invitee
10	Dr. M. Sarada	Professor, Department of ECE, VFSTR	School Dean Nominee

In the beginning of the meeting the Chairperson of the BoS, Dr. Polamraju V.S. Sobhan, Associate Professor and Head, department of EEE welcomed all the members and briefed them about the progress of the Department.

The following are the views expressed by the external members Dr. Jithendranath J

- Suggested to introduce case studies related to electrical engineering in the courses such as Deep Learning for Electrical Engineering
- Explained the need to encourage students to take up industry related problems as minor or mini projects.
- Recommended to include the AI based programming experiments in industrial drives and control.

Dr. B. Satish Babu

- Appreciated introduction of a course on deep learning for electrical engineering
- Proposed to include courses such as DSP applications in power electronics applications.

The following resolutions made after the discussion:

Approved the following SWAYAM-NPTEL courses as Electives

- > Smart Grid: Basics to Advanced Technologies
- Sensors and Actuators

The Chair rendered the closing remarks by thanking all the external members and internal members for their participation.

Member Secretary

Chairperson

The following are the members present for the board of studies meeting held at Department of Electrical and Electronics Engineering on 21.09.2023

Sl. No.	Name of the Member	Designation	Signature		
External Members:					
	Dr. Jithendranath J.	Senior Project Engineer, Hitachi Energy Technology Services P Ltd · Grid and Power Quality Solutions 2/4 A Mount Tower Mount Poonamallee Road, Chennai – 600089, Tamil Nadu Mobile: +91-9949537586 Email :jithendranath.j@hitachienergy.com	Participated the meeting virtually		
2	. Dr. B. Satish Babu	Sr. Staff Engineer, Infineon Technologies, Bangalore, Phone: 9958006750			
Inte	rnal Members:				
1.	Dr. Polamraju V.S. Sobhan	Associate Professor & HoD, Dept. of EEE, VFSTR	myt		
2.	Dr. G. Srinivasa Rao	Professor, Dept. of EEE, VFSTR	SVA		
3.	Dr. K. Mercy Rosalina	Professor, Dept. of EEE, VFSTR	Bush		
4.	Dr. M. Umamaheswara Rao	Asst. Professor, Dept. of EEE, VFSTR	m. v. Z.		
5.	Dr. M. Subba Rao	Associate Professor, Dept. of EEE, VFSTR	Liny		
6.	Dr. K. Balakrishna	Associate Professor, Dept. of EEE, VFSTR	a Borer		
7.	Dr. A.R. Vijay Babu	Associate Professor, Dept. of EEE, VFSTR	ARiní		
8.	Dr. M. Sarada	Professor, Department of ECE, VFSTR	m		

Chairperson